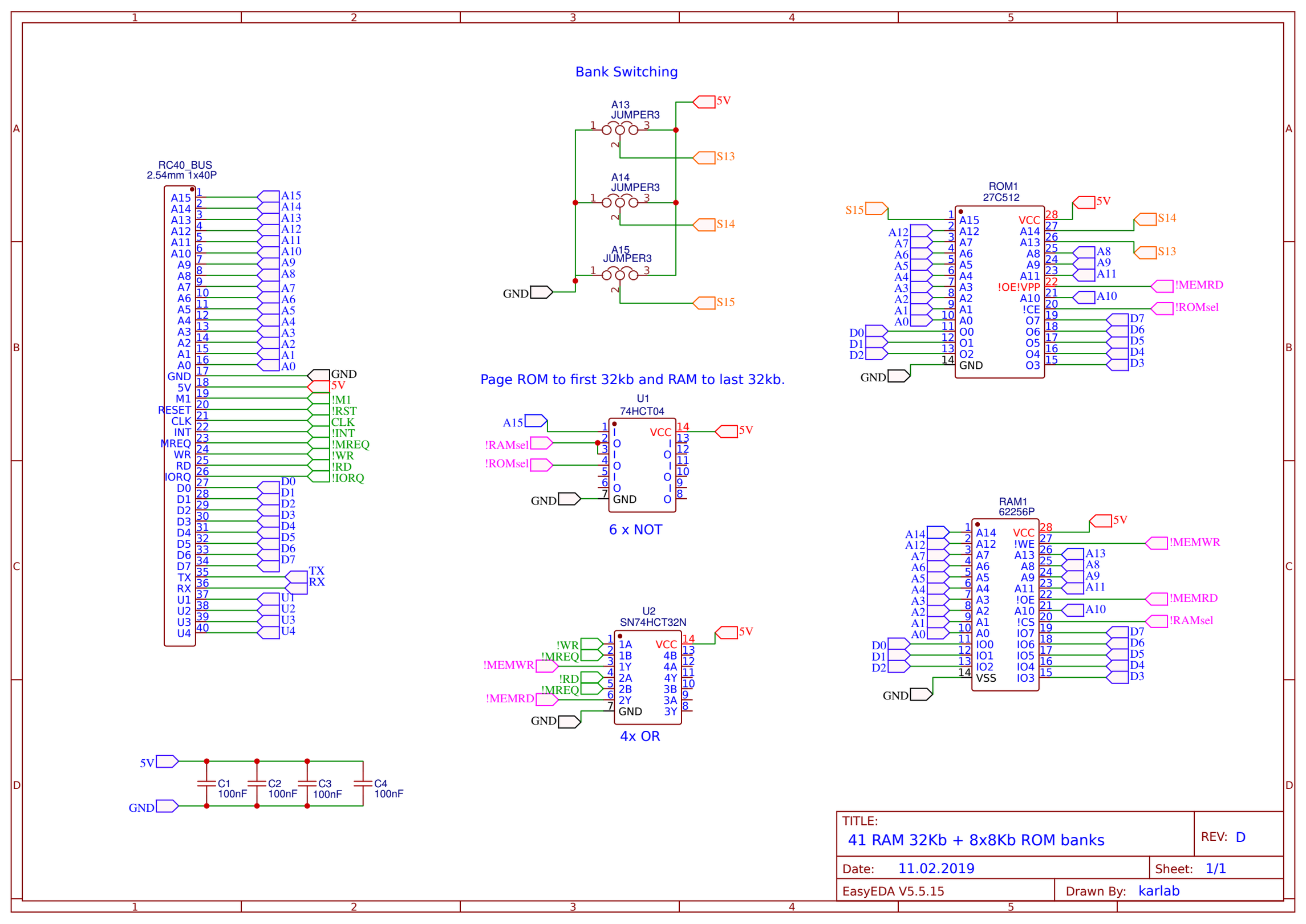
Bank Switching
A13
JUMPER3
A14
JUMPER3
A15
JUMPER3
5V
S13
S14
S15
GND
RC40_BUS
2.54mm 1x40P
A15
A14
A13
A12
A11
A10
A9
A8
A7
A6
A5
A4
A3
A2
A1
A0
GND
5V
M1
RESET
CLK
INT
MREQ
WR
RD
IORQ
D0
D1
D2
D3
D4
D5
D6
D7
TX
RX
U1
U2
U3
U4
!M1
!RST
CLK
!INT
!MREQ
!WR
!RD
!IORQ
Page ROM to first 32kb and RAM to last 32kb.
U1
74HCT04
A15
!RAMsel
!ROMsel
VCC
6 x NOT
U2
SN74HCT32N
!WR
!MREQ
!MEMWR
!RD
!MEMRD
4x OR
ROM1
27C512
S15
S14
S13
A12
A7
A6
A5
A4
A3
A2
A1
A0
!OE!VPP
A10
!CE
!MEMRD
!ROMsel
RAM1
62256P
!WE
!MEMWR
!RAMsel
VSS
C1
100nF
C2
100nF
C3
100nF
C4
100nF
TITLE:
41 RAM 32Kb + 8x8Kb ROM banks
REV: D
Date: 11.02.2019
Sheet: 1/1
EasyEDA V5.5.15
Drawn By: karlab